



2824

**FEE TRANSMITTAL SHEET
(FOR FY 2002)****Complete if Known**

Application No.	10/043,682
Filing Date	January 10, 2002
First Named Inventor	Timothy B. Cowles et al.
Group Art Unit	2824
Examiner	Van Thu T. Nguyen
Atty. Docket Number	500792.05

METHOD OF PAYMENT (Check One)

1. ☒ The Commissioner is hereby authorized to charge any additional fee required under 37 C.F.R. §§ 1.16 and 1.17 and 1.136(a)(3) and credit any over payments to Deposit Account No.: 50-1266; Deposit Account Name: DORSEY & WHITNEY LLP

2. ☐ Check Enclosed

FEE CALCULATION**1. BASIC FILING FEE**

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
101	750	201	375	<input type="checkbox"/> Utility Filing Fee
106	330	206	165	<input type="checkbox"/> Design Filing Fee
108	750	208	375	<input type="checkbox"/> Reissue Filing Fee
114	160	214	80	<input type="checkbox"/> Provisional Filing Fee

Subtotal (1) \$0**2. EXTRA CLAIM FEES**

Current Claims	Prior	Extra	Fee	Fee Paid
Total	8	- 20	= 0 x \$ 18	= \$ 0
Ind.	4	- 4	= 0 x \$ 84	= \$ 0
Multiple Dependent Claims			x \$	= \$

Subtotal (2) \$0

Large Entity		Small Entity		Fee Description
Fee Code	Fee (\$)	Fee Code	Fee (\$)	
103	18	203	9	Claims in excess of 20
102	84	202	42	Independent claims in excess of 3
104	280	204	140	Multiple dependent Claim
109	84	209	42	Reissue independent claims over original patent
110	18	210	9	Reissue claims in excess of 20 and over original patent

FEE CALCULATION (Continued)**3. ADDITIONAL FEES**

Large Entity		Small Entity		Fee Description	Fee paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
105	130	205	65	Surcharge - Late filing fee or oath	\$
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	\$
139	130	139	130	Non-English specification	\$
147	2,520	147	2,520	For Filing a Request for Reexamination	\$
195	300	196	300	Publication (early or Republication)	\$
115	110	215	55	Extension for reply within first month	\$
116	410	216	205	Extension for reply within 2 nd month	\$
117	930	217	465	Extension for reply within 3 rd month	\$
118	1,450	218	725	Extension for reply within 4 th month	\$
128	1,970	280	985	Extension for reply within 5 th month	\$
120	320	220	160	Filing a brief in support of an appeal	\$
121	280	270	140	Request for oral hearing	\$
148	110	248	55	Terminal Disclaimer Fee	\$
140	110	240	55	Petition to revive - unavoidable	\$
141	1,300	241	650	Petition to revive - unintentional	\$
142	1,300	242	650	Utility/Reissue issue fee (+ advance copies)	\$
143	470	243	235	Design issue fee (+ advance copies)	\$
122	130	122	130	Petitions to the Commissioner	\$
123	50	123	50	Petitions related to provisional applications	\$
126	180	126	180	Submission of IDS	\$
581	40	81	40	Recording each patent assignment per property (times number of properties)	\$
179	750	279	375	Request for Continued Examination (RCE)	\$

Other fee (specify)

Subtotal (3) \$0**Total Amount of Payment: \$0****Submitted by:**

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Signature:

Date:

February 25, 2002

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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Box Non-Fee Amendment, Commissioner of Patents, Washington, DC 20231.

Date

February 26, 2003

Denise Sheridan

Denise Sheridan

6/8 Andh
3-11-03
R. Wall

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Timothy B. Cowles et al.

Attorney Docket No.: 500792.05

Serial No. : 10/043,682

Group Art Unit : 2824

Filed : January 10, 2002

Examiner : Van Thu T. Nguyen

Title : REFRESH CONTROLLER AND ADDRESS REMAPPING CIRCUIT AND METHOD
FOR DUAL MODE FULL/REDUCED DENSITY DRAMS

Box Non-Fee Amendment
Commissioner of Patents
Washington, DC 20231

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AMENDMENT

Sir:

Applicants acknowledge receipt of the Office Action dated January 22, 2003.

In the Specification:

Please replace the paragraph beginning at page 16, line 26, with the following rewritten paragraph:

As mentioned above, the column address buffers 34 are designed to remap one of the row address to a column address so that the SDRAM 20 is plug compatible with SDRAMs adapted to receive row addresses for M rows and column addresses for N/2 columns. With reference to Figure 4, a portion of the column address buffers 34 includes a latch 260 that receives ten column address bits CA0-CA9. The column address buffer 260 latches the address bids responsive to an active low column address strobe read/write command, as is well known in the art. The nine low order address bits CA0-CA8 are applied to the column decoder 56 (Figure 1) in a conventional manner. However, the most significant column address bit CA9 is applied

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